## 450mA/500mA Current-Limit Switches


#### Abstract

General Description The MAX4795-MAX4798 family of switches feature internal current limiting to prevent damage to host devices due to faulty load conditions. These analog switches have a low $0.2 \Omega$ on-resistance and operate from a 2.0 V to 4.5 V input voltage range. They are available with guaranteed 450 mA and 500 mA current limits, making them ideal for SDIO and other load-switching applications.

When the switch is on and a load is connected to the port, a guaranteed blanking time of 14 ms ensures the transient voltages settle down. If after this blanking time, the load current is greater than the current limit, the MAX4795 and MAX4797 switches are turned off and $\overline{F L A G}$ is issued to the microprocessor. The switch can be turned on again by cycling the power or the ON input. The MAX4796 and MAX4798 have an autoretry feature where the switch turns off and issues a FLAG to the microprocessor after the blanking time and then continuously checks to see if the overload condition is present. The switch remains on after the overload condition disappears and FLAG deasserts. The MAX4795-MAX4798 are available in tiny, spacesaving, 5 -pin SOT23 and 6-pin TDFN ( $3 \mathrm{~mm} \times 3 \mathrm{~mm}$ ) packages.


Applications

## SDIO

PDAs and Palmtop Devices
Cell Phones
GPS Systems
Hand-Held Devices

## Typical Operating Circuit



Features

- Guaranteed Current Limit: 450 mA and 500 mA
- Thermal-Shutdown Protection
- Reverse-Current Protection
- $0.2 \Omega$ On-Resistance
- 14ms Guaranteed Blanking Time
- FLAG Function
- Autoretry (MAX4796/MAX4798)
- 80~AA Supply Current
- 6 AA Latchoff Current
(MAX4795/MAX4797)
- $0.01 \mu \mathrm{~A}$ Shutdown Current
- +2V to +4.5V Supply Range
- Fast Current-Limit Response Time
- Tiny SOT23 and TDFN Packages
- UL Certification Pending

Ordering Information

| PART | TEMP RANGE | PIN- <br> PACKAGE | TOP <br> MARK |
| :--- | :--- | :--- | :---: |
| MAX4795EUK-T | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ | 5 SOT23-5 | AEDI |
| MAX4795ETT* | $40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ | 6 TDFN-EP** | - |
| MAX4796EUK-T | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ | 5 SOT23-5 | AEDJ |
| MAX4796ETT | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ | 6 TDFN-EP** | - |
| MAX4797EUK-T | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ | 5 SOT23-5 | AEDK |
| MAX4797ETT* | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ | 6 TDFN-EP** | - |
| MAX4798EUK-T | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ | 5 SOT23-5 | AEDL |
| MAX4798ETT* | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ | 6 TDFN-EP** | - |

*Future product-contact factory for availability.
${ }^{* *} E P=$ Exposed pad.
Selector Guide appears at end of data sheet.
Pin Configurations


## 450mA/500mA Current-Limit Switches

## ABSOLUTE MAXIMUM RATINGS

IN, ON, FLAG, OUT to GND .....................................-. 0.3 V to +6 V
OUT Short Circuit to GND............................nternally Limited
Continuous Power Dissipation $\left(\mathrm{T}_{\mathrm{A}}=+70^{\circ} \mathrm{C}\right)$
5-Pin SOT23 (derate $7.1 \mathrm{~mW} /{ }^{\circ} \mathrm{C}$ above $+70^{\circ} \mathrm{C}$ )........... 571 mW
6-Pin TDFN (derate $24.4 \mathrm{~mW} /{ }^{\circ} \mathrm{C}$ above $+70^{\circ} \mathrm{C}$ ) ........ 1951 mW

Operating Temperature Range ........................... $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ Junction Temperature ..................................................... $150^{\circ} \mathrm{C}$ Storage Temperature Range ............................. $-65^{\circ} \mathrm{C}$ to $+150^{\circ} \mathrm{C}$ Lead Temperature (soldering, 10s) ................................. $+300^{\circ} \mathrm{C}$ 6 -Pin TDFN (derate $24.4 \mathrm{~mW} /{ }^{\circ} \mathrm{C}$ above $+70^{\circ} \mathrm{C}$ ) ......... 1951 mW Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

## ELECTRICAL CHARACTERISTICS

$\left(\mathrm{V}_{\mathrm{IN}}=+2.0 \mathrm{~V}\right.$ to $+4.5 \mathrm{~V}, \mathrm{~T}_{\mathrm{A}}=-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$, unless otherwise noted. Typical values are at $\mathrm{V}_{\mathrm{IN}}=3.3 \mathrm{~V}, \mathrm{~T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$.) (Note 1$)$

| PARAMETER | SYMBOL | CONDITIONS |  | MIN | TYP | MAX | UNITS |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Operating Voltage | $\mathrm{V}_{\text {IN }}$ |  |  | 2.0 |  | 4.5 | V |
| Quiescent Current | IQ | $\mathrm{V}_{\text {ON }}=\mathrm{V}_{\text {IN }}$, IOUT $=0$, switch on | $\mathrm{V}_{\mathrm{IN}}=+3.3 \mathrm{~V}$ |  | 80 | 125 | $\mu \mathrm{A}$ |
| Latch-Off Current | ILATCH | $\mathrm{V}_{\mathrm{ON}}=\mathrm{V}_{\text {IN }}$, after an overcurrent fault (MAX4795/MAX4797) (Note 2) | $\mathrm{V}_{\mathrm{IN}}=+3.3 \mathrm{~V}$ |  | 6 | 10 | $\mu \mathrm{A}$ |
| Shutdown Current | ISHDN | $\mathrm{V}_{\mathrm{ON}}=0 \mathrm{~V}$ |  |  | 0.03 | 1 | $\mu \mathrm{A}$ |
| Forward Current Limit |  | MAX4795/MAX4796, VIN = 3.3V, VOUT = GND |  | 450 |  | 720 | mA |
|  |  | MAX4797/MAX4798, VIN =3.3V, VOUT = GND |  | 500 |  | 800 |  |
| Reverse Current Limit |  | MAX4795/MAX4796 |  |  |  | 720 | mA |
|  |  | MAX4797/MAX4798 |  | 800 |  |  |  |
| ON Input Leakage |  |  |  | -1 |  | +1 | $\mu \mathrm{A}$ |
| Shutdown Forward Leakage |  | $\mathrm{V}_{\text {ON }}=0 \mathrm{~V}, \mathrm{~V}_{\text {OUT }}=0 \mathrm{~V}$ |  |  | 0.01 | 1 | $\mu \mathrm{A}$ |
| Shutdown Reverse Leakage |  | $\mathrm{V}_{\text {ON }}=0 \mathrm{~V}, \mathrm{~V}_{\text {IN }}=2 \mathrm{~V}, \mathrm{~V}_{\text {OUT }}=4.5 \mathrm{~V}$ |  |  |  | 1 | $\mu \mathrm{A}$ |
| On-Resistance (Note 3) | Ron | MAX4795/MAX4796, ILOAD $=100 \mathrm{~mA}$ |  |  | 0.2 | 0.4 | $\Omega$ |
|  |  | MAX4797/MAX4798, ILOAD $=100 \mathrm{~mA}$ |  |  | 0.18 | 0.36 |  |
| ON Input Logic High Voltage | $\mathrm{V}_{\mathrm{IH}}$ | $\mathrm{V}_{\mathrm{IN}}=+2 \mathrm{~V}$ to +3.6 V |  | 1.4 |  |  | V |
|  |  | V IN $>3.6 \mathrm{~V}$ |  | 2 |  |  |  |
| ON Input Logic Low Voltage | VIL |  |  |  |  | 0.5 | V |
| $\overline{\text { FLAG }}$ Output Logic Low Voltage |  | ISINK $=1 \mathrm{~mA}$ |  |  |  | 0.4 | V |
| $\overline{\text { FLAG }}$ Output High Leakage Current |  | $\mathrm{V}_{\mathrm{IN}}=\mathrm{V}_{\overline{\mathrm{FLAG}}}=\mathrm{V}_{\mathrm{ON}}=4.5 \mathrm{~V}$ |  |  |  | 1 | $\mu \mathrm{A}$ |
| Thermal Shutdown |  |  |  |  | 150 |  | ${ }^{\circ} \mathrm{C}$ |
| Thermal-Shutdown Hysteresis |  |  |  |  | 15 |  | ${ }^{\circ} \mathrm{C}$ |
| DYNAMIC |  |  |  |  |  |  |  |
| Turn-On Time |  | $\mathrm{V}_{\text {ON }}$ from low to high; lout = 10mA, $\mathrm{CL}_{\mathrm{L}}=0.1 \mu \mathrm{~F}$ (Note 4) |  | 120 |  |  | $\mu \mathrm{s}$ |
| Turn-Off Time |  | VON from high to low; lout = 10mA, CL $=0.1 \mu \mathrm{~F}$ (Note 4) |  | 100 |  |  | ns |
| Blanking Time | tBLANK | Overcurrent fault; Figures 2, 3 |  | 14 |  | 60 | ms |
| Short-Circuit Current-Limit Response Time |  | VON $=$ VIN, short circuit applied to OUT |  | 5 |  |  | $\mu \mathrm{s}$ |
| Retry Time | tretry | MAX4796/MAX4798, Figure 2 (Note 5) |  | 210 |  | 900 | ms |

Note 1: All parts are $100 \%$ tested at $+25^{\circ} \mathrm{C}$. Electrical limits across the full temperature range are guaranteed by design and correlation.
Note 2: Latch-off current does not include the current flowing into $\overline{F L A G}$.
Note 3: TDFN packages are guaranteed by design.
Note 4: The on-time is defined as the time taken for the current through the switch to go from OmA to full load. The off-time is defined as the time taken for the current through the switch to go from full load to 0mA.
Note 5: Retry time is typically 15 times the blanking time.
2

## 450mA/500mA Current-Limit Switches

Typical Operating Characteristics
$\left(\mathrm{V}_{\mathrm{IN}}=3.3 \mathrm{~V}, \mathrm{~T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}\right.$, unless otherwise noted. $)$


## 450mA/500mA Current-Limit Switches



## 450mA/500mA Current-Limit Switches

## Typical Operating Characteristics (continued)

$\left(\mathrm{V}_{\mathrm{IN}}=3.3 \mathrm{~V}, \mathrm{~T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}\right.$, unless otherwise noted. $)$


Pin Description

| PIN |  | NAME | FUNCTION |
| :---: | :---: | :---: | :--- |
| SOT23 | TDFN |  | IN |
| 1 | 1 | Input. Bypass with a 0.1 $1 \mu$ F ceramic <br> capacitor to ground. |  |
| 2 | 5 | GND | Ground |
| 3 | 6 | ON | Active-High Switch On Input. A logic <br> high turns the switch on. |
| 4 | 4 | FLAG | Fault Output. This open-drain output <br> goes low when the device stays in <br> forward or reverse current limit for <br> more than the blanking time period. <br> FLAG is high impedance when a fault <br> is not present or when ON is low. |
| 5 | 2 | OUT | Switch Output. Bypass with a 0.1 <br> ceramic capacitor to ground. |
| - | 3 | N.C. | No Connection. Not internally <br> connected. |



Figure 1. Functional Diagram

# 450mA/500mA Current-Limit Switches 

## Detailed Description

The MAX4795-MAX4798 are forward/reverse currentlimited switches that operate from a 2 V to 4.5 V input voltage range and guarantee a 450 mA and 500 mA minimum current-limit threshold for different options. The voltage drop across an internal sense resistor is compared to two reference voltages to indicate a forward or reverse current-limit fault. When the load current exceeds the preset current limit for greater than the fault-blanking time, the switch opens.
The MAX4796 and MAX4798 have an autoretry function that turns on the switch again after an internal retry time expires. If the faulty load condition is still present after the blanking time, the switch turns off again and the cycle is repeated. If the faulty load condition is not present, the switch remains on.
The MAX4795 and MAX4797 do not have the autoretry option and the switch remains in latch-off mode until the ON pin or the input power is cycled from high to low and then high again.

## Reverse-Current Protection

The MAX4795-MAX4798 limit the reverse current (VOUT to $V_{I N}$ ) from exceeding the maximum IREV value. The switch is shut off and FLAG is asserted if the reverse current-limit condition persists for more than the blanking time. This feature prevents excessive reverse currents from flowing through the device.

## Switch-On/Off Control

Toggle ON high to enable the current-limited switches. The switches are continuously on if there is no fault. When a forward/reverse current fault is present or the die exceeds the thermal-shutdown temperature of $+150^{\circ} \mathrm{C}$, OUT is internally disconnected from IN and the supply current decreases to $8 \mu \mathrm{~A}$ (latch off). The switch is now operating in one of its off states. The switch-off state also occurs when driving ON low, thus reducing the supply current (shutdown) to $0.01 \mu \mathrm{~A}$. Table 1 illustrates the ON/OFF state of the MAX4795-MAX4798 current-limit switches.
$\overline{\text { FLAG }}$ Indicator
The MAX4795-MAX4798 feature a latched output (FLAG). Whenever an overcurrent condition is encountered, the MAX4795/MAX4797 latch FLAG low and turn the switch off. The MAX4796/MAX4798 latch FLAG low and keep it low until the overcurrent condition is removed. During this time, the switch cycles on and off in the autoretry mode. When the overcurrent condition is removed, $\overline{\text { FLAG }}$ deasserts and the switch turns on (Figure 2). FLAG is an open-drain output transistor and requires an external pullup resistor from FLAG to IN. During shutdown ( ON is low), the pulldown on $\overline{\mathrm{FLAG}}$ output is released to limit power dissipation. FLAG goes low when any of the following conditions occur:

- The die temperature exceeds the thermal-shutdown temperature limit of $+150^{\circ} \mathrm{C}$.
- The device is in current limit for more than the faultblanking period.
- The switch is in autoretry.


Figure 2. MAX4796/MAX4798 Autoretry Fault-Blanking Diagram

Table 1. MAX4795-MAX4798 Switch Truth Table

| ON | FAULT | SWITCH ON/OFF | SUPPLY CURRENT MODE |
| :---: | :---: | :--- | :---: |
| Low | X | OFF | Shutdown |
| High | Thermal | OFF immediately (tBLANK period does not apply) | Latchoff |
| High | Current limit | OFF after tBLANK period has elapsed | Latchoff |
|  |  | See the Autoretry section |  |

## 450mA/500mA Current-Limit Switches

## Autoretry (MAX4796/MAX4798)

When the forward or reverse current-limit threshold is exceeded, tBLANK timer begins counting (Figure 2). The timer resets if the overcurrent condition disappears before tBLANK has elapsed. A retry time delay, tRETRY, is started immediately after tBLANK has elapsed and during that time, the switch is latched off and FLAG asserts. At the end of tRETRY, the switch is turned on again. If the fault still exists, the cycle is repeated. If the fault has been removed, the switch stays on and FLAG deasserts.
The autoretry feature saves system power in the case of an overcurrent or short-circuit condition. During tBLANK, when the switch is on, the supply current is at the current limit. During tRETRY, when the switch is off, the current through the switch is zero. Instead of observing the full load current, the switch sees the equivalent load current times duty cycle or ISUPPLY $=$ ILOAD $\times$ tblank/(tblank + tretry). With a typical tBLANK $=37 \mathrm{~ms}$ and typical tRETRY $=555 \mathrm{~ms}$, the duty cycle is $6 \%$, which results in a $94 \%$ power savings over the switch being on the entire time. The duty cycle is consistent across the process and devices.

## Latchoff (MAX4795/MAX4797)

When the forward or reverse current-limit threshold is exceeded, tBLANK timer begins counting. The timer resets if the overcurrent condition disappears before tBLANK has elapsed. The switch is shut off and FLAG asserts if the overcurrent condition continues up to the end of the blanking time. Reset the switch by either toggling ON (Figure 3) or cycling the input voltage.

Fault Blanking The MAX4795-MAX4798 feature 14 ms ( min ) fault blanking. Fault blanking allows current-limit faults, including momentary short-circuit faults that occur when hot swapping a capacitive load, and also ensures that no fault is issued during power-up. When a load transient causes the device to enter current limit, an internal counter starts. If the load-transient fault persists beyond the fault-blanking timeout, FLAG asserts low. Load-transient faults less than tBLANK do not cause a FLAG output assertion. Only current-limit faults are blanked.
A thermal fault causes $\overline{F L A G}$ to assert immediately and does not wait for the blanking time.

## Thermal Shutdown

The MAX4795-MAX4798 have a thermal-shutdown feature to protect the devices from overheating. The switch


Figure 3. MAX4795/MAX4797 Latch-Off Fault Blanking
turns off and $\overline{\text { FLAG }}$ goes low immediately (no fault blanking) when the junction temperature exceeds $+150^{\circ} \mathrm{C}$. The switches with autoretry turn back on when the device temperature drops approximately $15^{\circ} \mathrm{C}$. The switches with latchoff require ON cycling.

## Applications Information

## Input Capacitor

To limit the input voltage drop during momentary output short-circuit conditions, connect a capacitor from IN to GND. A $0.1 \mu \mathrm{~F}$ ceramic capacitor is adequate for most applications; however, higher capacitor values further reduce the voltage drop at the input and are recommended for lower voltage applications.

## 450mA/500mA Current-Limit Switches

Output Capacitance Connect a $0.1 \mu \mathrm{~F}$ capacitor from OUT to GND. This capacitor helps prevent inductive parasitics from pulling OUT negative during turn off, thus preventing the MAX4795-MAX4798 from tripping erroneously. If the load capacitance is too large, then current may not have enough time to charge the capacitor and the device assumes that there is a faulty load condition. The maximum capacitive load value that can be driven from OUT is obtained by the following formula:

$$
\mathrm{C}_{\mathrm{MAX}}<\frac{\text { IFWD_MIN } \times \text { tbLANK_MIN }}{\mathrm{V}_{\mathrm{IN}}}
$$

## Layout and Thermal Dissipation

To optimize the switch response time to output shortcircuit conditions, it is very important to keep all traces as short as possible to reduce the effect of undesirable parasitic inductance. Place input and output capacitors as close as possible to the device (no more than 5 mm ). IN and OUT pins must be connected with short traces to the power bus.

Selector Guide

| PART | CURRENT LIMIT <br> $(\mathbf{m A})$ | AUTORETRY |
| :--- | :---: | :---: |
| MAX4795EUK-T | 450 | No |
| MAX4795ETT | 450 | No |
| MAX4796EUK-T | 450 | Yes |
| MAX4796ETT | 450 | Yes |
| MAX4797EUK-T | 500 | No |
| MAX4797ETT | 500 | No |
| MAX4798EUK-T | 500 | Yes |
| MAX4798ETT | 500 | Yes |

During normal operation, the power dissipation is small and the package temperature change is minimal. If the output is continuously shorted to ground at the maximum supply voltage, the operation of the switches with the autoretry option does not cause problems because the total power dissipated during the short is scaled by the duty cycle:

$$
P_{\text {MAX }}=\frac{V_{\text {IN_MAX }} \times I_{\text {OUT_MAX }} \times t_{\text {BLANK }}}{t_{\text {RETRY }}+t_{\text {BLANK }}}=211 \mathrm{~mW}
$$

where $\mathrm{VIN}=4.5 \mathrm{~V}$, IOUT $=750 \mathrm{~mA}$, tBLANK $=14 \mathrm{~ms}$, and tRETRY $=210 \mathrm{~ms}$.
Attention must be given to the MAX4795 and MAX4797 where the latchoff condition must be manually reset by toggling ON from high to low. If the latchoff time duration is not sufficiently high, it is possible for the device to reach the thermal-shutdown threshold and never be able to turn the device on until it cools down.

## Chip Information

TRANSISTOR COUNT: 2539
PROCESS: BICMOS

## 450mA/500mA Current-Limit Switches

Package Information
(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information, go to www.maxim-ic.com/packages.)

SIDE VIEW

| SYMBLL | MIN | MAX |
| :---: | :---: | :---: |
| A | 0.90 | 1.45 |
| A1 | 0.00 | 0.15 |
| A2 | 0.90 | 1.30 |
| $b$ | 0.35 | 0.50 |
| C | 0.08 | 0.20 |
| D | 2.80 | 3.00 |
| E | 2.60 | 3.00 |
| E1 | 1.50 | 1.75 |
| L | 0.35 | 0.60 |
| L1 | 0.60 REF |  |
| e | 0.95 BSC. |  |
| e1 | 1.90 BSC. |  |
| a | $0^{\circ}$ | $8^{\circ}$ |

